



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Group Art Unit: 2811  
Examiner: Junghwa M. Im

In re PATENT APPLICATION of

Applicant : Kazutaka SHIBATA )

Appln. No. : 10/608,016 )

Filed : June 30, 2003 )

For : SEMICONDUCTOR DEVICE AND )  
MANUFACTURING METHOD THEREOF )

Atty. Dkt. : AI 288 )

) **AMENDMENT WITH**  
) **RCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Final Examiner's Action mailed June 15, 2006, an RCE being submitted herewith, please amend the above-identified application as follows: